Application No. 10/010,273
Reply to Office Action of June 22, 2005

## Amendments to the Specification

As suggested by the Examiner, please replace the Title with the following:

MEMORY DEVICE HAVING ADDRESS GGENERATING CIRCUIT USING
PHASE ADJUSTMENT BY SAMPLING DIVIDED CLOCK TO GENERATE

ADDRESS SIGNAL OF SEVERAL BITS HAVING ONE BIT CHANGED IN
SEQUENTIAL ORDER